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22387 U.S. PTO  
10/757415



**Mail Stop Patent Application**

**Commissioner for Patents**  
**P.O. Box 1450**  
**Alexandria, VA 22313-1450**

New U.S. Patent Application

Title: SIMULATION CIRCUIT PATTERN EVALUATION METHOD,  
MANUFACTURING METHOD OF SEMICONDUCTOR INTEGRATED  
CIRCUIT, TEST SUBSTRATE, AND TEST SUBSTRATE GROUP

Inventors' Names and Residences:

- 1) Hisashi KANEKO  
Kanagawa, Japan
- 2) Motoya OKAZAKI  
Tokyo, Japan
- 3) Hiroyuki TOSHIMA  
Saitama, Japan

Sir:

We enclose the following papers for filing in the United States Patent and Trademark Office in connection with the above patent application.

1. Application - 31 pages, including 4 independent claims and 22 claims total.
2. Drawings - 10 sheets of formal drawings containing 16 figures - (Figures 1-5).
3. Certified copy of Japanese Application No. 2003-344526, filed October 2, 2003.
4. Information Disclosure Statement and Information Disclosure Citation, PTO 1449 with 2 documents attached.
5. The filing fee is calculated as follows:

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|   |                     |   |       |                 |        |           |
|---|---------------------|---|-------|-----------------|--------|-----------|
| Basic Application Filing Fee                                    |                     |   |       |                 | \$770  | \$ 770.00 |
|   | Number of<br>Claims |   | Basic | Extra<br>Claims |        |           |
| Total Claims  | 22                  | - | 20    | 2               | x \$18 | \$ 36.00  |
| Independent Claims  | 4                   | - | 3     | 1               | x \$86 | \$ 86.00  |
| <input type="checkbox"/> Presentation of Multiple Dep. Claim(s) |                     |   |       |                 | +\$290 |           |
| Subtotal  |                     |   |       |                 |        | \$ 892.00 |
| Reduction by 1/2 if small entity                                |                     |   |       |                 |        | -         |
| TOTAL APPLICATION FILING FEE                                    |                     |   |       |                 |        | \$ 892.00 |

6. A check for \$892.00 is enclosed. The fee includes:

\$770.00 filing fee; and  
\$122.00 additional claims fee

This application is being filed under the provisions of 37 C.F.R. § 1.53(f).  
Applicants await notification from the Patent and Trademark Office of the time set for filing the Declaration.

7. Applicants claim the right to priority based on Japanese Application No. 2003-344526, filed October 2, 2003.

Please address all correspondence with respect to this application to:

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Please accord this application an application number and filing date.

**Commissioner for Patents**  
January 15, 2004  
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The Commissioner is hereby authorized to charge any additional filing fees due and any other fees due under 37 C.F.R. § 1.16 or §1.17 during the pendency of this application to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,  
GARRETT & DUNNER, L.L.P.

By: \_\_\_\_\_

  
Richard V. Burgujian  
Reg. No. 31,744

RVB/FPD/blc  
Enclosures